

General Description

The epc660-BC (Board-to-Board Chip Carrier) is an easy-to-use board with an epc660 chip (fully integrated 3D-TOF imager with a resolution of 320 x 240 pixels, QVGA). It allows a simple mounting and interconnection to a PCB board which carries the necessary illumination and application system. The lens mounting for standard lenses with lens holders is also easily be done.

This board is well suited for small and medium volume production of 3D TOF cameras.

Only few additional components are needed to generate a complete 3D camera. Depending on illumination power and optical design, a resolution in the millimeter range for distances up to dozens of meters is feasible. Up to 158 full frame TOF images are delivered in rolling mode. The extremely high sensitivity of the chip allows for a reduced illumination power and reduced overall power consumption compared to other TOF imagers.

An evaluation kit for the epc660 is available with hard- and software examples and a comprehensive manual to speed up system integration.

Features

- epc660 chip assembled on carrier for easy-to-use application
- Easy lens mounting by using standard lens mounts and lenses
- Well suited for small and medium volume production

Applications

- People detection and counting
- Mobile postal parcel size measurement
- Machine safety
- Drone collision avoidance sensing
- Car collision avoidance systems
- Pedestrian detection and breaking systems
- Man-Machine interface
- Gesture control
- Body size measurement
- General volumetric mapping
- Mobile robotics
- Simultaneous localization and mapping (SLAM)

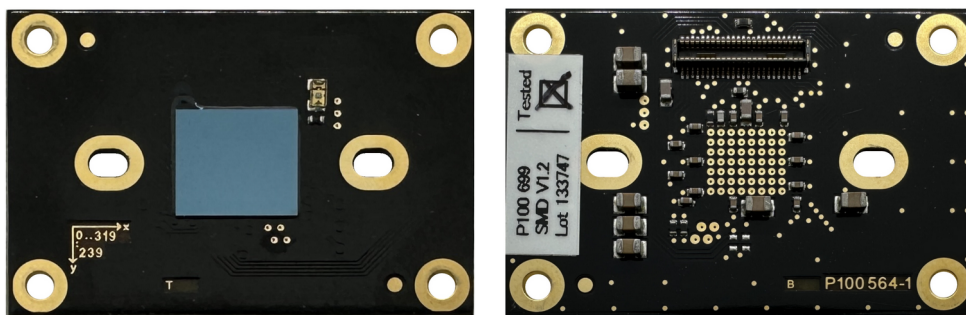


Figure 1: epc660 BC Chip Carrier

1. Ordering information

Part #	Part Name	Description	Package	RoHS
P100 699	epc660-011-BC-003	epc660 on chip carrier board to board connector	PCB 37.4 x 25.40 x 3.00 mm	yes

Table 1: Ordering Information

2. Operation

This module contains the epc660 chip and all necessary passive components which have to be close to the epc660 chip. The board is designed to be connected to a Hirose DF40GB-48DS connector. All pins necessary to operate the epc660 chip are accessible by this connector. The operation description of the epc660 chip is described in the datasheet epc660.

3. Schematic diagram

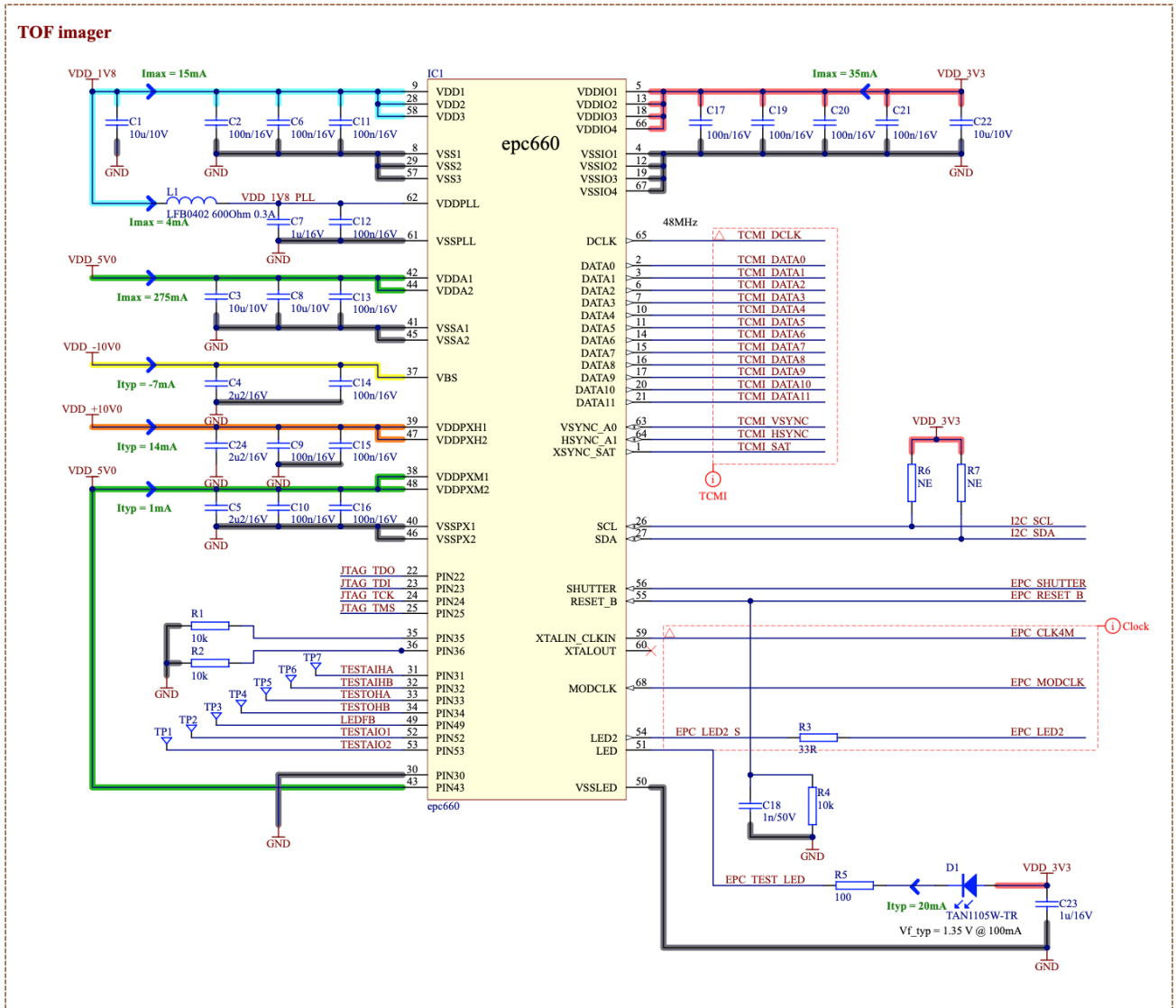


Figure 2: Schematic diagram - TOF imager

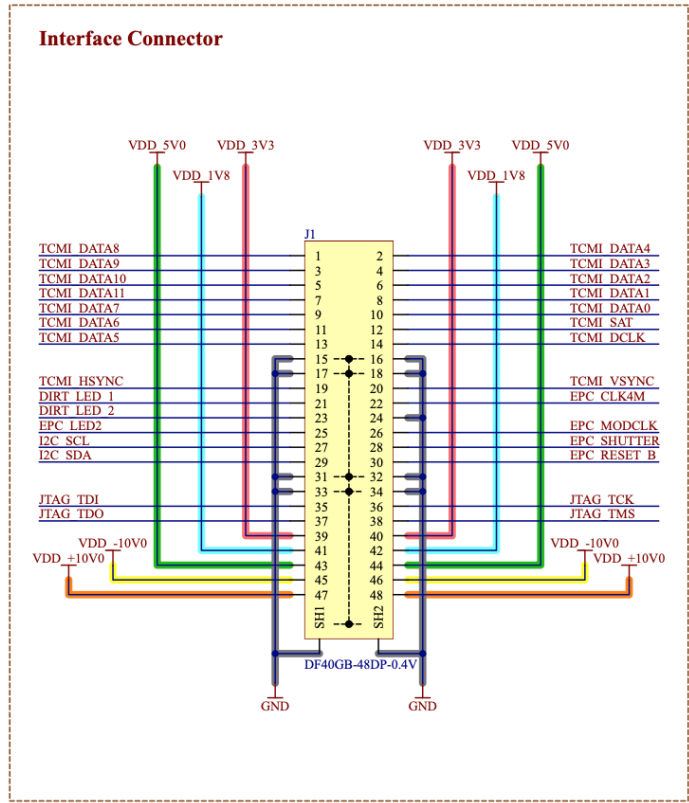


Figure 3: Schematic diagram - Interface Connector

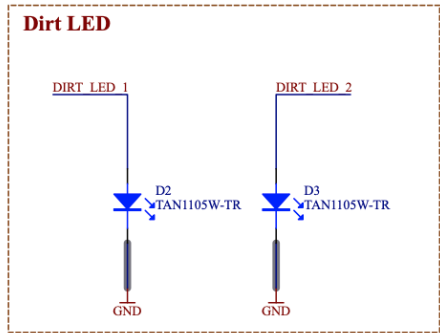


Figure 4: Schematic diagram - Dirt LED

5. Mechanical dimensions

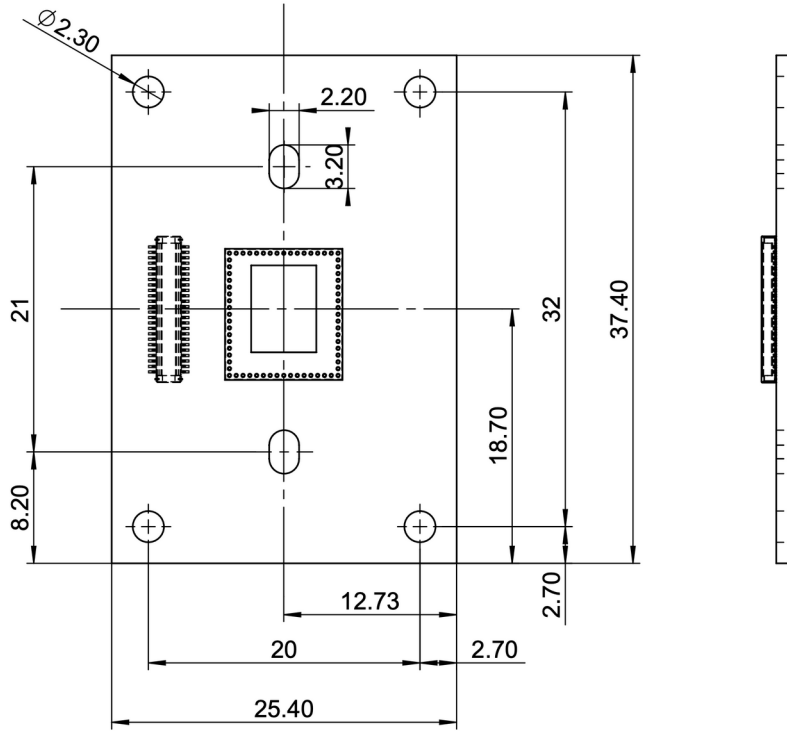


Figure 8: Dimensions
(in mm, bottom view, PCB material is glass epoxy FR-4, thickness 1.6mm)

6. Socket

Figure 9 shows the Hirose Electric J Board-to-Board connector from the DF40 Series. For more information, please refer to the DF40GB(3.0)-48DS-0.4V(51) data sheet.

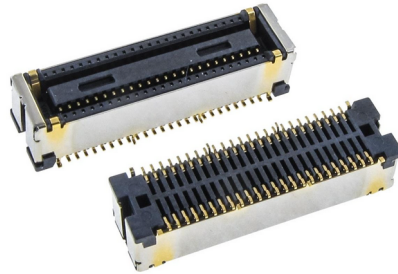


Figure 9: Vertical Board-to-Board connector

7. Change history

Chip Carrier Version	Changes
001	First release

8. IMPORTANT NOTICE

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